## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No Filed Herewith							
Filing Date Filed Herewith							
Inventor Luan C. Tran							
Assignee Micron Technology, Inc.							
Group Art Unit							
Examiner Unknown							
Attorney's Docket No							
Title: Methods of Forming Conductive Capacitor Plugs, Methods of Forming Capacitor							
Contact Openings, and Methods of Forming Memory Arrays							

## INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending application Serial No. 09/359,956, filed July 22, 1999. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

ed: <u>7303</u>

James D. Shaurette Reg. No. 39,833

By:

Form PTO-1449				U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY, DOCKET NO. MI22-2339			SERIAL NO. Filed Herewith		
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*Examiner Initial		Document Number		Date	Name		Class	Subs	class	Filing Date If Appropriate		
_	AA	5,780,338		07/14/98	Jeng et al.		<del> </del>					
	AB	5,706,164		01/06/98	Jeng							
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<b> </b>	AN	06029408		02/04/94	EPO Japan & English translation		ļ	ــــ		Abs.		
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				OTHER REFE	ERENCES (including Author, Title, Date, P	•						
	AR		Serial No. 08/998,023,	Parekh et al.; file	ekh et al.; filed December 24, 1997.							
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